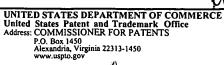


UNITED STATES PATENT AND TRADEMARK OFFICE





APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/755,285	01/04/2001	William Joshua Price	M-9949 US	4689
32566	7590 11/18/2003		EXAM	INER
PATENT LAW GROUP LLP 2635 NORTH FIRST STREET			RIOS CUEVAS, I	ROBERTO JOSE
SUITE 223			ART UNIT	PAPER NUMBER
SAN JOSE, CA 95134			2836	

DATE MAILED: 11/18/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)		
	09/755,285	PRICE, WILLIAM JOSHUA		
Office Action Summary	Examiner	Art Unit		
	Roberto J Rios	2836		
The MAILING DATE of this communication Period for Reply	appears on the cover sheet w	th the correspondence address		
A SHORTENED STATUTORY PERIOD FOR RETHE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication - If the period for reply specified above is less than thirty (30) days, and If NO period for reply is specified above, the maximum statutory period for reply within the set or extended period for reply will, by soon - Any reply received by the Office later than three months after the meanmed patent term adjustment. See 37 CFR 1.704(b).	ON. R 1.136(a). In no event, however, may a real to the control of the control o	eply be timely filed y (30) days will be considered timely. THS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).		
1)⊠ Responsive to communication(s) filed on 1	0 September 2003.			
	his action is non-final.			
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.				
Disposition of Claims	o/ panto quajio, 1000 0.2	,		
4) ⊠ Claim(s) 1,5,7 and 9-21 is/are pending in the day of the above claim(s) is/are with 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) 1,5,7 and 9-21 is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction are	drawn from consideration.			
Application Papers 9) The specification is objected to by the Exam 10) The drawing(s) filed on is/are: a)		by the Examiner.		
Applicant may not request that any objection to	, , , , , , , , , , , , , , , , , , , ,			
Replacement drawing sheet(s) including the co	rrection is required if the drawing	(s) is objected to. See 37 CFR 1.121(d).		
11) The oath or declaration is objected to by the	e Examiner. Note the attached	d Office Action or form PTO-152.		
Priority under 35 U.S.C. §§ 119 and 120				
12) Acknowledgment is made of a claim for for a) All b) Some * c) None of: 1. Certified copies of the priority docum 2. Certified copies of the priority docum 3. Copies of the certified copies of the application from the International Bu * See the attached detailed Office action for a 13) Acknowledgment is made of a claim for domesince a specific reference was included in the	nents have been received. nents have been received in A priority documents have been reau (PCT Rule 17.2(a)). list of the certified copies not lestic priority under 35 U.S.C.	pplication No received in this National Stage received. § 119(e) (to a provisional application)		
37 CFR 1.78. a) ☐ The translation of the foreign language 14) ☐ Acknowledgment is made of a claim for dom reference was included in the first sentence of	estic priority under 35 U.S.C.	§§ 120 and/or 121 since a specific		
1) Notice of References Cited (PTO-892)	4) Interview S	Summary (PTO-413) Paper No(s)		
Notice of Draftsperson's Patent Drawing Review (PTO-948 Information Disclosure Statement(s) (PTO-1449) Paper No) 5) Notice of I	offormal Patent Application (PTO-152)		
U.S. Patent and Trademark Office PTOL-326 (Rev. 11-03) Office	e Action Summary	Part of Paper No. 8		

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Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

2. Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over AAPA in view of Byers et al (5,422,915).

As per claim 1, AAPA (page 2, line 15) teaches that RAID arrays are housed in disk enclosures, wherein devices such as disk drives within said enclosures run off power supplies but does not specifically disclose the claimed power domain arrangement. However, Byers et al (herein after Byers) teach a data storage array comprising a first power domain consisting of a plurality of data storage means being powered by a first group of current sharing power supplies, a second power domain consisting of a plurality of data storage means being powered by a second group of current sharing power supplies (Figure 5).

It would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify AAPA's RAID array with Byers power domains arrangement for the purpose of providing security against voltage loss and ensure data integrity.

3. Claims 5, 7 and 9-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over AAPA in view of Byers, and further in view of Wiscombe et al (US patent 5,668,417).

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As per claim 5, AAPA in view of Byers teaches a power domain arrangement but fails to disclose the claimed shared power domain. However, Wiscombe et al (herein after Wiscombe) teach two separate power supplies, wherein a power circuit is provided to provide shared power to a third plurality of elements for said two power supplies (Figure 2).

It would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify AAPA's in view of Byers power domains arrangement with Wiscombe's shared power domain for the purpose of ensuring standby power if either one of the power sources fail.

As per claims 10 and 11, AAPA in view of Byers and Wiscombe teaches the claimed power domain arrangement including the third shared power domain but fails to provide a fourth shared power domain. However, it is been held that there is no invention in duplicating parts or components to provide a multiplied effect. In the instant case, Wiscombe already teaches the obviousness of providing a separate shared power domain receiving power from tow separate power sources and outputting shared power to third and separate elements. Thus, it would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify AAPA's in view of Byers and Wiscombe as a matter of engineering design to provide multiple shared power domains for the purpose of ensuring standby power to multiple elements if either one of the power sources fail.

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As per claims 7 and 12, AAPA in view of Byers and Wiscombe teaches the claimed circuit except providing a fuse. However, the Examiner takes official notice that it is well known in the power redundancy art to provide a diode in series with a fuse.

It would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify AAPA's in view of Byers and Wiscombe's such that a fuse is included in the power circuit for the purpose of providing load isolation from an overvoltage condition.

As per claim 9, Byers teaches a backup power source but does not specifically disclose using a battery. However, the Examiner takes official notice that backup batteries are well known in the power redundancy art.

It would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify AAPA's in view of Byers and Wiscombe's such that a backup battery is provided for the purpose of providing auxiliary DC power in case of a faulty power source.

As per claims 13-16, AAPA teaches that the claimed elements are housed within RAID arrays disk enclosures. Thus, it would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify AAPA's in view of Byers and Wiscombe's such that different elements are selectively grouped together for the purpose of providing power redundancy among said elements.

4. Claims 17-21 are rejected under 35 U.S.C. 103(a) as being unpatentable over AAPA in view of Byers and Wiscombe as applied to claim 16 above, and further in view of White (US patent 6,260,079).

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As per claims 17 and 20, AAPA teaches a RAID array but does not specifically disclose the claimed port bypass circuit. However, White teaches a disk drive array (Figures 12, 13B) comprising a backplane controller coupled to a port bypass circuit operable to bypass a disk drive (*col. 20, line 64; col. 21, line 25)

It would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify AAPA's in view of Byers and Wiscombe's such that a bypass port circuit is provided for the purpose of bypassing any disk drive element for hot-swapping purposes.

As per claim 18, AAPA in view of Byers and Wiscombe teaches the claimed power domain arrangement including the third shared power domain but fails to provide a fourth shared power domain. However, it is been held that there is no invention in duplicating parts or components to provide a multiplied effect. In the instant case, Wiscombe already teaches the obviousness of providing a separate shared power domain receiving power from tow separate power sources and outputting shared power to third and separate elements. Thus, it would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify AAPA's in view of Byers and Wiscombe as a matter of engineering design to provide multiple shared power domains for the purpose of ensuring standby power to multiple elements if either one of the power sources fail.

AAPA in view of Byers and Wiscombe teaches the claimed circuit except providing a fuse. However, the Examiner takes official notice that it is well known in the power redundancy art to provide a diode in series with a fuse.

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It would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify AAPA's in view of Byers and Wiscombe's such that a fuse is included in the power circuit for the purpose of providing load isolation from an overvoltage condition.

As per claims 19 and 21, AAPA teaches that the claimed elements are housed within RAID arrays disk enclosures and run off power supplies. Thus, it would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify AAPA's in view of Byers and Wiscombe's as a matter of engineering design such that different elements are selectively grouped together for the purpose of providing power redundancy among said elements.

Response to Arguments

- 5. Applicant's arguments with respect to claims 1, 5, 7 and 9-21 have been considered but are moot in view of the new ground(s) of rejection.
- 6. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any

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extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Communication with PTO

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 308-0956. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Roberto Rios whose telephone number is (703) 306-5518. In the event that Examiner Rios cannot be reached, his supervisor, Brian Sircus may be contacted at (703) 308-3119. The fax number for Before-Final communications and After-Final communications is (703) 872-9306.

BRIAN SIRCUS SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800